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CONFIRMATION NO. ATTORNEY DOCKET NO. FIRST NAMED INVENTOR FILING DATE APPLICATION NO. 4961 020124 Yutaka Mimino 03/06/2002 10/090,610 09/04/2003 23850 7590 ARMSTRONG,WESTERMAN & HATTORI, LLP EXAMINER HO, TU TU V 1725 K STREET, NW **SUITE 1000** WASHINGTON, DC 20006 PAPER NUMBER ART UNIT

DATE MAILED: 09/04/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

	_		A.	
		Application No.	Applicant(s)	
		10/090,610	MIMINO ET AL.	
,	Office Action Summary	Examiner	Art Unit	
•		Tu-Tu Ho	2818	
_	The MAILING DATE of this communication	tion appears on the cover sh	neet with the correspondence address	
	or Reply	DEDLY IS SET TO EXPIR	F 2 MONTH/S) EDOM	
THE - External control	MAILING DATE OF THIS COMMUNICA ensions of time may be available under the provisions of 3 or SIX (6) MONTHS from the mailing date of this communicate period for reply specified above is less than thirty (30) do period for reply is specified above, the maximum statute to reply within the set or extended period for reply will, reply received by the Office later than three months after need patent term adjustment. See 37 CFR 1.704(b).	ATION. 7 CFR 1.136(a). In no event, however cation. ays, a reply within the statutory minimuly period will apply and will expire SIX by statute. Cause the application to be	may a reply be timely filed m of thirty (30) days will be considered timely. (6) MONTHS from the mailing date of this communical come ABANDONED (35 U.S.C. § 133).	tion.
1)[Responsive to communication(s) filed	on <u>19 June 2003</u> .		
2a)□)⊠ This action is non-fina	l.	
3)		or allowance except for form	nal matters, prosecution as to the merit	s is
Disposi	tion of Claims	s diluci ex pullo dadjio, il		
•	Claim(s) 1 and 3-7 is/are pending in the	ne application.		
٠, ـــــ	4a) Of the above claim(s) is/are		on.	
5)				
, —	Claım(s) <u>1,3,6 and 7</u> is/are rejected.			
	Claim(s) <u>4 and 5</u> is/are objected to.			
•	Claim(s) are subject to restriction	on and/or election requirem	ent.	
	tion Papers			
] The specification is objected to by the I			
10)] The drawing(s) filed on is/are: a)□ accepted or b)□ objected	I to by the Examiner.	
	Applicant may not request that any object	ction to the drawing(s) be held	in abeyance. See 37 CFR 1.85(a).	
11)	The proposed drawing correction filed			
	If approved, corrected drawings are requ		on.	
] The oath or declaration is objected to b	by the Examiner.		
	under 35 U.S.C. §§ 119 and 120		11 0 0 0 440(a) (d) == (f)	
	Acknowledgment is made of a claim for	or foreign priority under 35	U.S.C. § 119(a)-(d) or (f).	
6	a)⊠ All b)□ Some * c)□ None of:			
	1. Certified copies of the priority d			
	2. Certified copies of the priority d			
	3. Copies of the certified copies of application from the Interna * See the attached detailed Office action	tional Bureau (PCT Rule 1)	ve been received in this National Stage 7.2(a)). Dies not received.	!
4.4\[-	See the attached detailed Office action Acknowledgment is made of a claim for	r domestic priority under 35	U.S.C. § 119(e) (to a provisional appli	cation).
14)	a) The translation of the foreign lang			,
15)[a) LI The translation of the foreign land Acknowledgment is made of a claim fo	r domestic priority under 35	5 U.S.C. §§ 120 and/or 121.	
Attachm			DTO 4401 D N. (1)	
2) N	otice of References Cited (PTO-892) otice of Draftsperson's Patent Drawing Review (PT formation Disclosure Statement(s) (PTO-1449) Pa	O-948) 5) 🔲	Interview Summary (PTO-413) Paper No(s) Notice of Informal Patent Application (PTO-152) Other:	

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DETAILED ACTION

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Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 1. 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under ExParte Quayle, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 19 June 2003 has been entered.

Claim Rejections

- Applicant is reminded that upon the cancellation of claims to a non-elected invention, the 2. inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a diligently filed petition under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(h).
- The figures and reference numbers referred to in this office action are used merely to 3. indicate an example of a specific teaching and are not to be taken as limiting.

Claim Rejections - 35 USC § 102

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The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United

(e) the invention was described in

(1) an application for patent, published under section 122(b), by another filed in the United States

before the invention by the applicant for patent or

(2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 3, and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Shinohara 4. et al. Japanese Patent Application Publication Number 03-077324 (cited by Applicant and hereinafter referred to as publication '324).

Publication '324 discloses in Figures 1-6 a multilayer wiring structure for semiconductor devices as claimed.

Referring to claim 1, the reference discloses a multilayer wiring structure for semiconductor devices, comprising:

a semiconductor substrate 12;

at least one active region 2 (Figure 1) supplied with an electric power from a powersupply potential (not shown);

a plurality of power-supply lines 7x/8x (Figures 1, 3, and 5) for supplying with the electric power to said active region therethrough, said power-supply lines disposed at different layers of the multilayer wiring structure on said semiconductor substrate and being connected in parallel to each other; and

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a common power-supply line 14 (Figures 4-6) provided between said power supply potential and said active region, the common power-supply line being connected to the power-supply lines and having a current-carrying capacity larger than that of each of the power-supply lines.

Referring to claim 3, publication '324 further discloses that at least one power-supply pad 10 (Figure 4) connecting to said power-supply potential, wherein said common power-supply line 14 is provided between said power-supply pad 10 and said power-supply lines 7/8 (Figure 5).

Referring to claim 7, the upper right corner of Figure 4 shows a plurality of power-supply pads 10 connecting in parallel to common power-supply lines 14. Since the plurality of power-supply pads would connect to a same potential and each of the common power-supply lines connects to the power-supply lines, it follows that the plurality of power-supply pads 10 connect in parallel to the power-supply lines, and therefore meets the limitation of "said power-supply lines connect to said power-supply potential by a plurality of power-supply pads connecting in parallel to said power-supply lines". Note that "connecting in parallel" is interpreted broadly as connecting to the same potential (power-supply pads 10 in the upper right corner of Figure 4 of publication '324 would connect to the same potential the same way power-supply pads 9 of Figure 8 of the present invention would connect to the same potential). Note also that in the upper right corner of Figure 4 of publication '324, numerical reference 14 should have been 10.

Claim Rejections - 35 USC § 103

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The following is a quotation of 35 U.S.C. §103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claim 6 is rejected under 35 U.S.C. §103(a) as being unpatentable over publication '324.

As noted above, publication '324 discloses that the power-supply lines 7/8 connect to the active region 2 of the at least one active region. Obviously the figures of the publication fail to disclose that said power-supply lines connect in parallel to the active regions as claimed.

Nevertheless, at the time the invention was made, it was customary in the semiconductor art, particularly in power transistor design, to parallel active regions of identical transistors to achieve a desire power output. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form, instead of just one active region, active regions 2 and parallel them to achieve a desire power output, and in doing so, one would have connecting in parallel power-supply lines to the active regions.

Allowable Subject Matter

6. Claims 4 and 5 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

In the examiner's opinion, it is not obvious to one of ordinary skill in the art at the time the invention was made to modify the prior art of record to have the limitations as recited in these claims.

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Specifically, one would have not formed the common peripheral region power-supply line 14 (publication '324) such that it would be situated between the active region 2 and the power supply lines 7/8. In doing so, the common peripheral region power-supply line would not have been in the peripheral region. And one would not be able to connect both end of the common power-supply line to the power-supply lines since one end of the common power-supply line is already connected to the connection pad and this one end is not in the cell region where the power supply lines are.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (703) 305-0086. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (703) 308-4910. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

Tu-Tu Ho

August 13, 2003

HOAI HO PRIMARY EXAMINER